

CR7508 Series

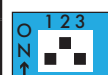
CR7508 Specifications		
Control Power Supply	SV1	SV2
	Universal AC Power Supply 85-240 Vac 50/60 Hz. 110-240 Vdc	12.5 - 35 Vdc 18-24 Vsc 50/60 Hz.
Copper Wire Only for Supply Connections		
Power Consumption	3 VA Max	2 Watts Max
Measurable Range	200% of Rated Full Scale, 50-400 Hz.	
Limiting Continuous Current	10ARMS for Rated Full Scale Otherwise 200% of Rated Full Scale	
Current Withstand	500A, 2 seconds	
Output Type	1 SPDT Form C Relay	
Terminals	(3) 0.25" Male Quick Connect	
Relay Output Ratings	Rated Voltage	250 VAC
	Rated Current	12 Amps
	Limiting Continuous Current	12 Amps
	Breaking Capacity MAX	3000 VA
	Electrical Endurance	>10 ⁵ Operations
	Mechanical Endurance	>30 X 10 ⁶ Operations
Trip Threshold	Adjustable or Fixed	
Time Delay	Adjustable or No Delay	
Hysteresis	5% of Full Scale Set Point	
Case	UL 94 5-VA Flammability Rating	
Environmental Ratings	Operating Temperature	-30C to +60C
	Storage Temperature	-55C to +85C
	Relative Humidity	0-95% RH, Non-condensing
	Altitude	< 2,000 Meters
Certifications	UL 1053 Listed, E211244 Vol. 2	

DIP Switch Settings:

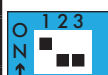
The DIP Switch settings allow for multiple reconfigurable options to meet various application needs. All DIP switch settings are latched at device start up. Any adjustments made to the DIP switch settings will need a power reset before taking effect.



DIP Switch setting 1 enables normally energized Form C Relay state logic. When enabled it causes the relay to energize during a NO FAULT condition. It's de-energized when control power is removed or a FAULT condition. See Relay Contact States table below.



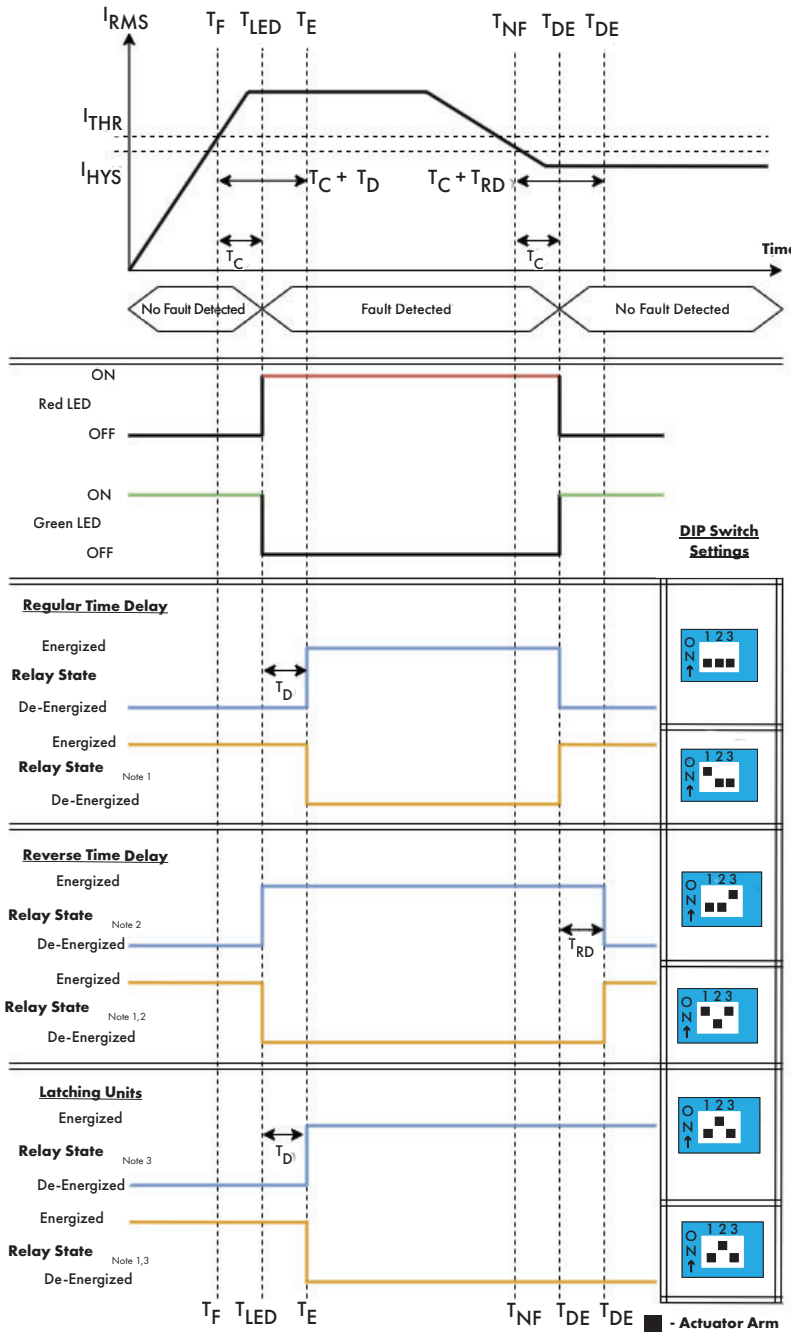
DIP Switch setting 2 enables device latching. Once ground fault currents rise above the adjustable or fixed threshold the Form C relay and red LED latch to a fault condition and will remain in a fault condition until either control power is removed or the "Latch Reset" button is activated.



DIP Switch setting 3 changes the settable relay to a reverse delay. Upon sensing a ground fault current greater than the settable or fixed threshold, the LED and Form C relay changes states immediately to a FAULT condition. Once currents drop below the settable or fixed threshold, the LED returns to a NO FAULT state and the delay timer starts. Once the delay timer has timed out the Form C relay is returned to the NO FAULT state. DIP SWITCH SETTING 3 IS IGNORED IF LATCHING IS ENABLED AND THE REGULAR SETTABLE DELAY IS OBSERVED.

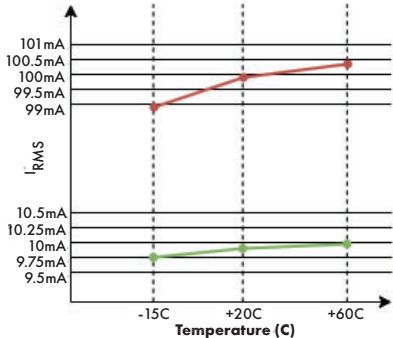
Relay Contact States							
		Power Applied					
		No Power		No Fault Condition		Fault Condition	
		Contact State	LED	Contact State	LED	Contact State	LED
DIP Switch setting 1 OFF (Normally De-Energized)	N.C.	Closed	OFF	Closed	Green	Open	Red
	N.O.	Open	OFF	Open	Green	Closed	Red
DIP Switch setting 1 ON (Normally Energized)	N.C.	Closed	OFF	Open	Green	Closed	Red
	N.O.	Open	OFF	Closed	Green	Open	Red

Ground-Fault Relay Timing Diagram

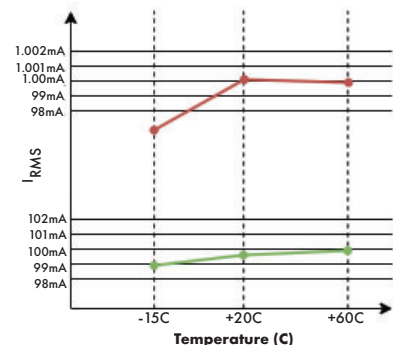


T_F = Time at which I_{RMS} exceeds settable current threshold.
 T_{LED} = Time at which the Red and Green LED changes state.
 T_E = Time at which the relay changes state to a fault condition.
 T_{NF} = Time at which I_{RMS} drops below I_{THR} plus hysteresis, I_{HYS}
 T_{DE} = Time at which the relay changes state to a no fault condition.
 T_C = Internal Calculation cycle, - 100mS.
 T_D = Settable Time Delay, if applicable.
 T_{RD} = Settable Reverse Time Delay, if applicable.

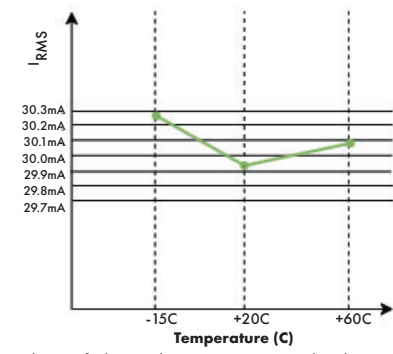
10mA to 100mA Trip Point Over Temperature Range



100mA to 1.00 A Trip Point Over Temperature Range



30mA Fixed Point Over Temperature Range



- Note 1:** Relay Energize state set by external DIP switch position 1. ON position causes relay to energize during a NO fault state and de-energize during a fault state. The OFF position causes the relay to reenergize during a fault state only.
- Note 2:** Reverse Time Delay set by external DIP switch position 3. ON position adds a settable delay time when I_{RMS} below I_{HYS} and when the relay changes states. With a reverse time delay, both the LED and relay change state at T_{LED} when I_{RMS} rises above I_{THR} . OFF position adds a settable delay time between when I_{RMS} rises above the settable current threshold, I_{THR} and when the relay changes state.
- Note 3:** External DIP switch position 2 enables or disables relay latching. Latching with a reverse time delay are NOT compatible. Reverse Time delay is ignored for latching parts and will behave like a regular time delay.
- Note 4:** Per UL 1053 Section 22, Table 22.1 I_{RMS} currents greater than 150% of I_{THR} must trip within 2.0 seconds and I_{RMS} currents greater than 250% of I_{THR} must trip within 1.0 second. If I_D is greater than the allowable delay, a 250mS max delay is used.
- Note 5:** DIP switch settings are latched at device start up. Power reset is necessary for DIP switch setting adjustment.

CR7508 Series

Ground-Fault Test Circuit:

The CR7508 Series includes an integrated test circuit which provides operational verification of the measuring current transformer, settable delay, LED's and the electro-mechanical relay. Upon pressing the "Circuit Test" button, a 3mA_{RMS} current is generated around a 10-turn test winding, simulating a 30mA_{RMS} Ground-Fault current.

Each unit's test circuit is factory calibrated and tested such that upon sensing the 30mA_{RMS} test current the device will indicate a fault condition by changing the LED and relay state to a fault condition. DIP switch settings 2 and 3 are ignored for test purposes; the device will observe the settable delay timer and latch to a fault condition (Reverse Time delays are not tested). The device remains in test mode until the "Latch Reset" button is pressed, at which point all original DIP switch settings will be reverted back.

NOTE: Internal test circuit does not satisfy the Performance Testing required by NEC 230.95(c). See Installation Test Procedures for more information.

NOTE: The test circuit is meant to be active while no current is flowing through the current transformer. If an external current is flowing through the current transformer while the test circuit is active, device behavior is undefined and may or may not indicate a fault condition.

Latch Reset:

A latch reset is included and performs two functions:

1. When DIP switch setting 2, Latching enable, is set, the latch reset button will return the CR7508 to a no fault condition if, the sensed ground fault current has fallen below the fixed or settable trip threshold plus hysteresis.
2. Stops the ground fault test circuit and resets to a no fault condition once the device has latched to a fault state.

If DIP switch setting 2 is disabled and the ground fault test circuit is inactive, the latch reset button has no effect.

Settable Delay:

The CR7508 internal digital timer allows for accurate and repeatable time delays.

Settable delay times include:

- C - Time delay, 100 Milliseconds to 1 second.
- X - Time delay, 10 Milliseconds to, non-adjustable

Customized Time delays available on request.

Per UL 1053, if ground fault currents exceed 150% of the set threshold current, a maximum of a 2 second delay is allowed; at 250% above the set threshold current a maximum of a 1 second delay is allowed.

Immediately following a calculation cycle, if the sensed ground fault current exceeds 150% of the threshold and the settable delay is set greater than 250 milliseconds, the delay defaults to 250 milliseconds. For over threshold current less than 150%, the normal settable delay is observed.

Fixed Point or Calibrated Dial:

The CR7508 is offered with either a fixed ground fault threshold or an adjustable range. A wide range of standard and customizable options are available for both fixed point and calibrated dial units upon request.

For custom adjustable trip ranges, a 10-100% scaling is used.
e.g. a 5mA minimum, 10%, will have a 50mA maximum, 100%.